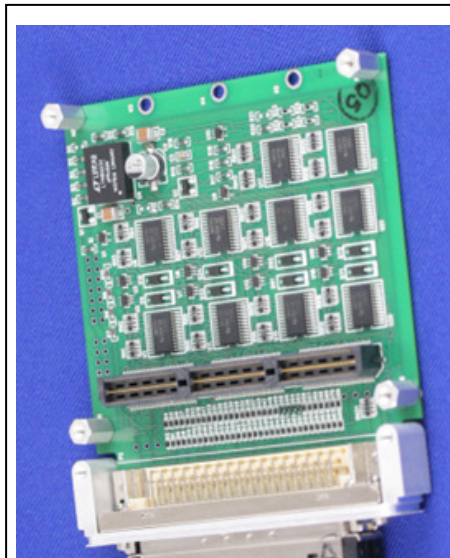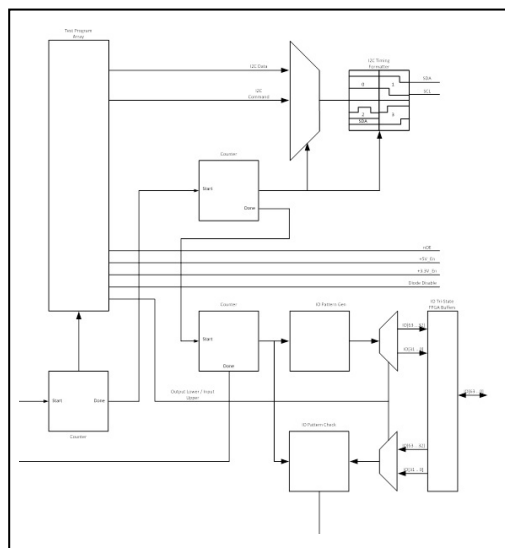# AXM FGA INTERFACE
## 64 Digital I/O Controller

## Features



- AXM interface board
- 64 I/O Channels
- Switch between 3.3-V LVTTL and 5V JEDEC standards (JESD8C.01
- Translate the two standards into an LVCMOS JEDEC standard for the FPGA pin connections.
- 64 I/O can be programmed to have resistors that pull the IO pins to the programmed VCC, or ground in banks of 8
- Power-up Controller to allow Pull-up or Pull down
- Front Panel MDR Connector

## Block Diagram and Operational Overview

The **Alphi Technology AXM-DIO64 module** enhances programmable logic boards with raw FPGA connections through a standard AXM connector. It is designed to switch seamlessly between **3.3V LVTTL** and **5V JEDEC standards** (JESD8C.01), translating these external I/O standards into the **LVCMOS JEDEC standard** for FPGA pin connections. The module also supports configurable pull-up or pull-down resistors in banks of 8, allowing the I/O pins to be programmed to the desired VCC or ground. The AXM-DIO64 provides a flexible and robust solution



| Ordering information | |
|---|---|
| Part Number | Description |
| **AXM-DIO64** | **AXM 64 Digital I/O** channels<br>**Option** (add following the part number)<br>**C** (Conformal coat) |
| | **Optional Accessories** |
| **CBL-MDR-68** | **68 Pins MDR Cable 1M** |